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| | Filing Date | March 01, 2002 |
| | First Named Inventor | Suresh M. Menon |
| | Examiner Name | RECEIVED |
| | Art Unit | 2631 |
| | Patent No. | OCT 20 2004 |
| Attorney Docket Number | Technology Center 2600 | |

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1050 US

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Suresh M. Menon et al.
Assignee: Xilinx, Inc.
Title: "High Speed Configurable Transceiver Architecture"

Serial No.: 10/090,250 File Date: 03/01/2002
Examiner: Unknown Art Unit: 2631
Docket No.: X-1050 US Conf. No.: 4098

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT


Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the four (4) references listed in the attached Substitute for Form PTO1449A/PTO. A copy of two references is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

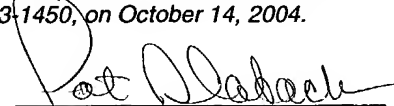
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Respectfully submitted,


Kim Kanzaki
Attorney for Applicants
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